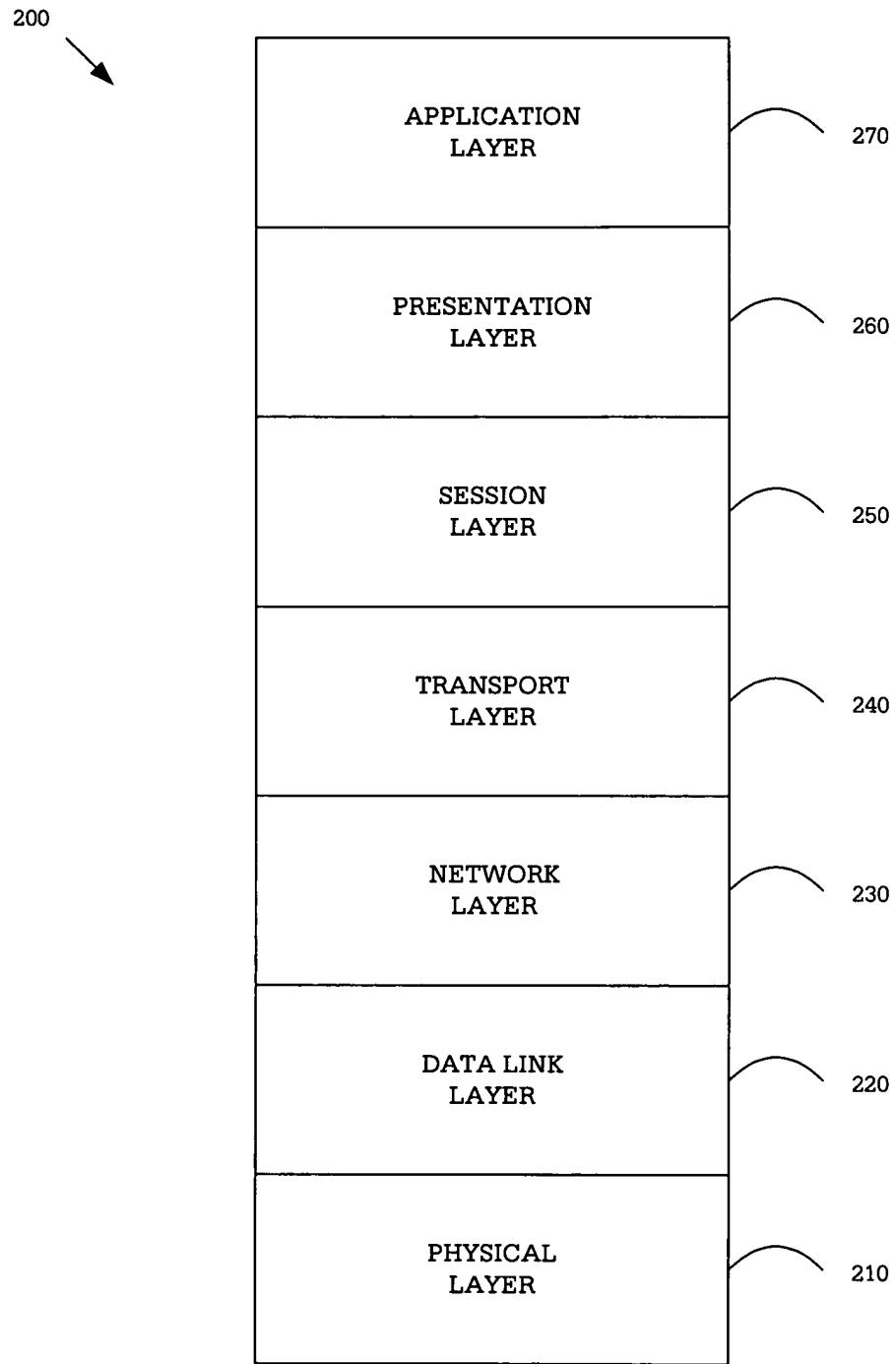
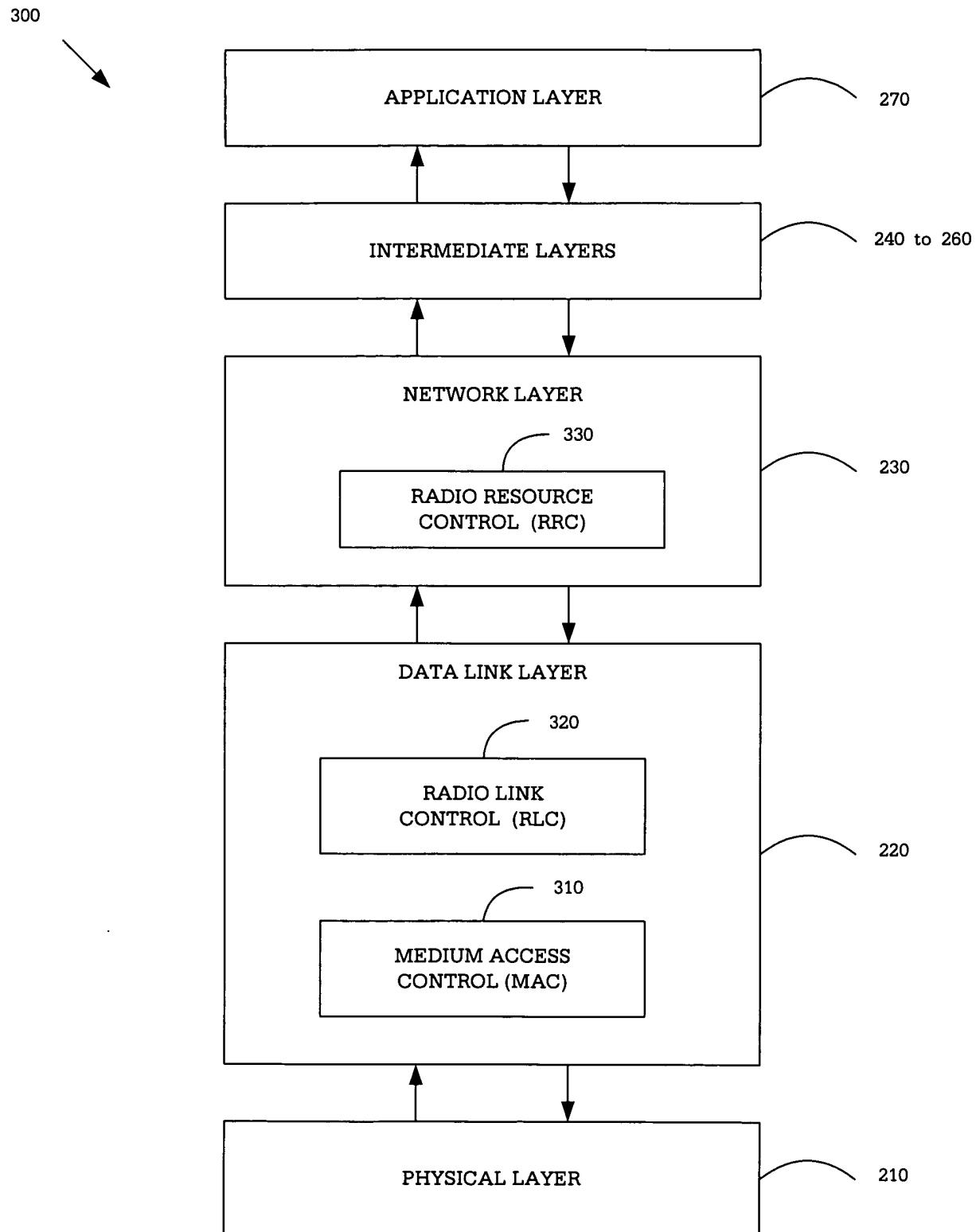


FIGURE 1

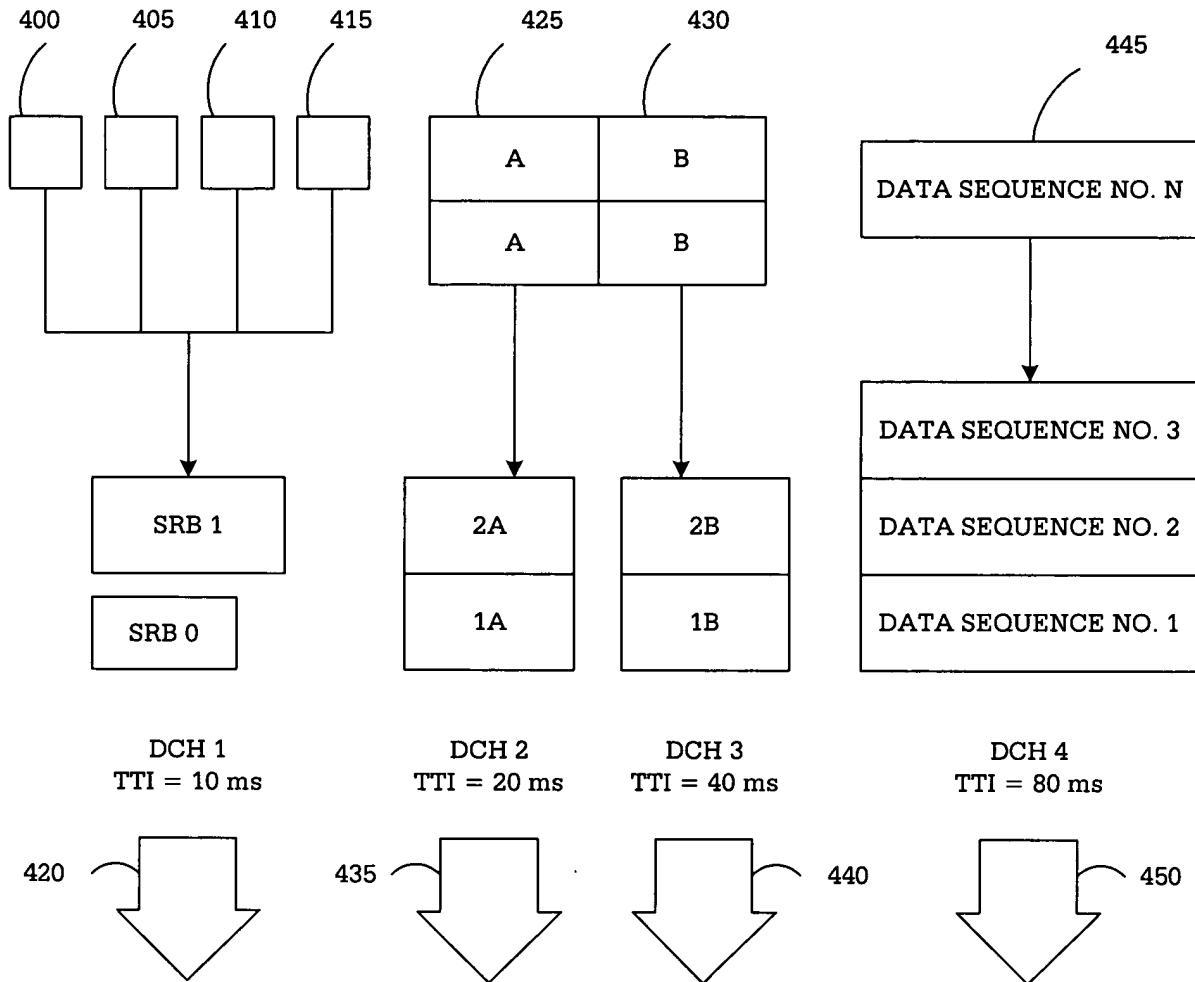


OPEN SYSTEMS INTERCONNECTION (OSI) LAYERS

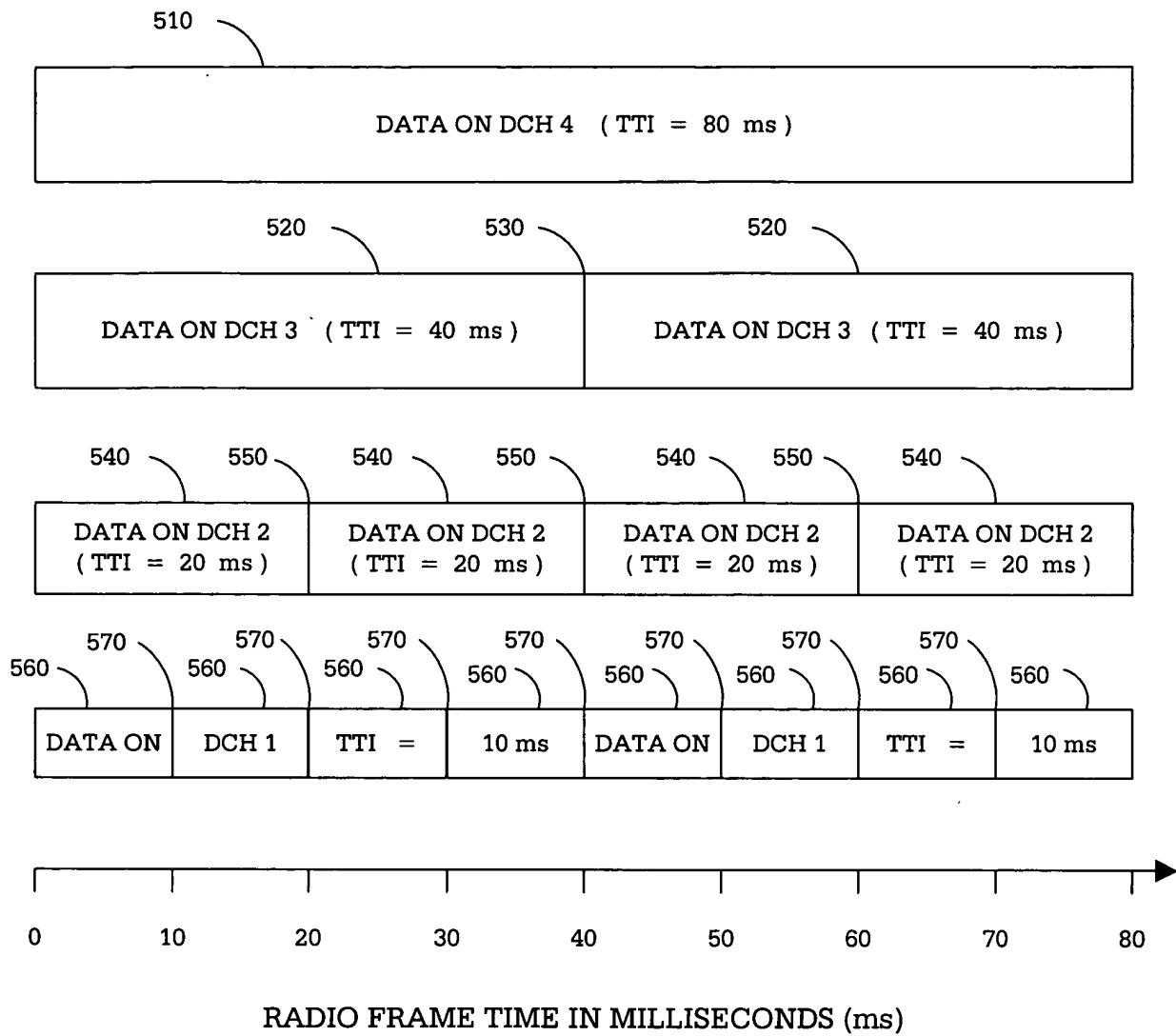
**FIGURE 2**  
PRIOR ART



**FIGURE 3**  
PRIOR ART



**FIGURE 4**  
PRIOR ART



**FIGURE 5**  
PRIOR ART

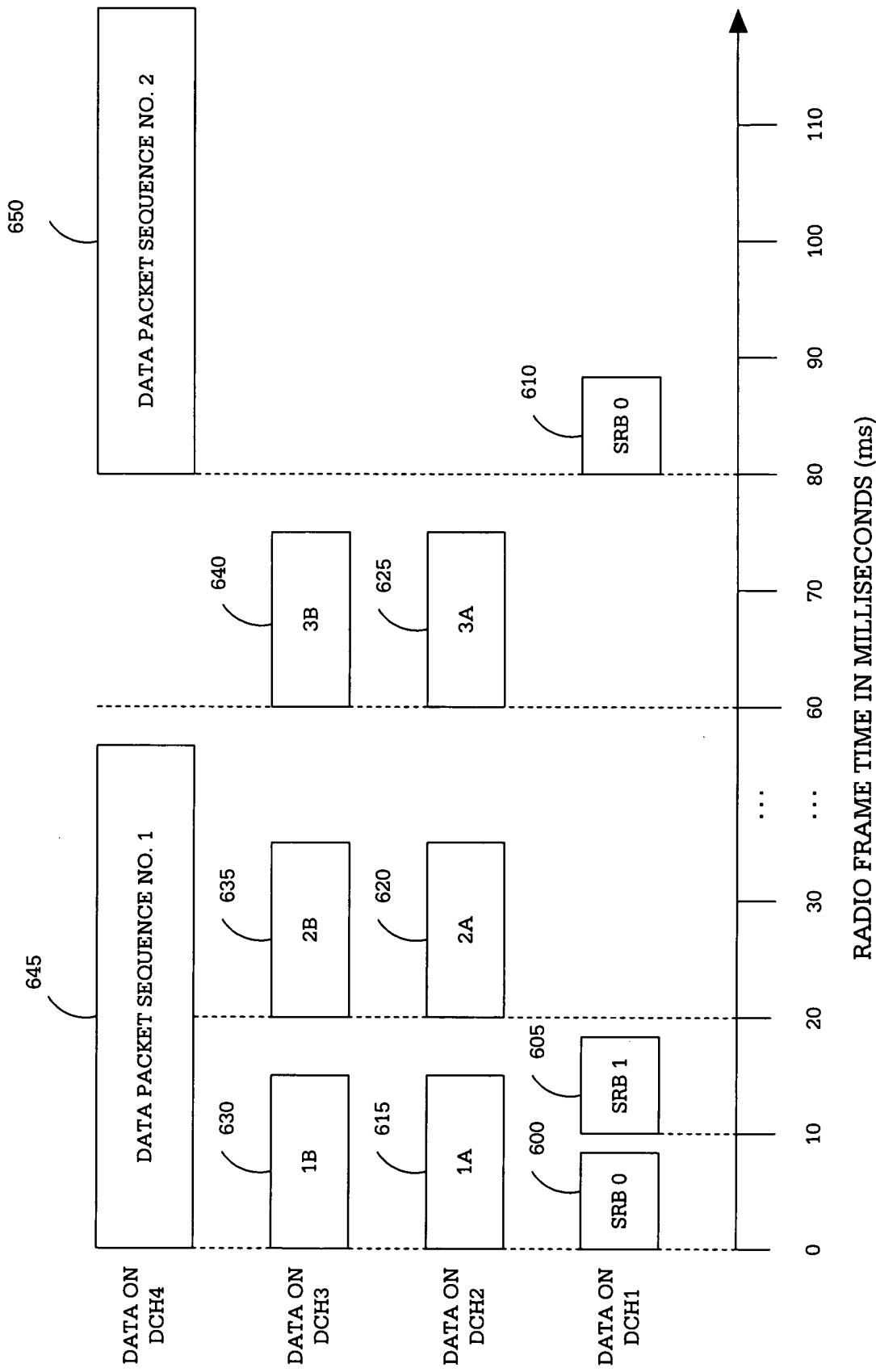


FIGURE 6  
PRIOR ART

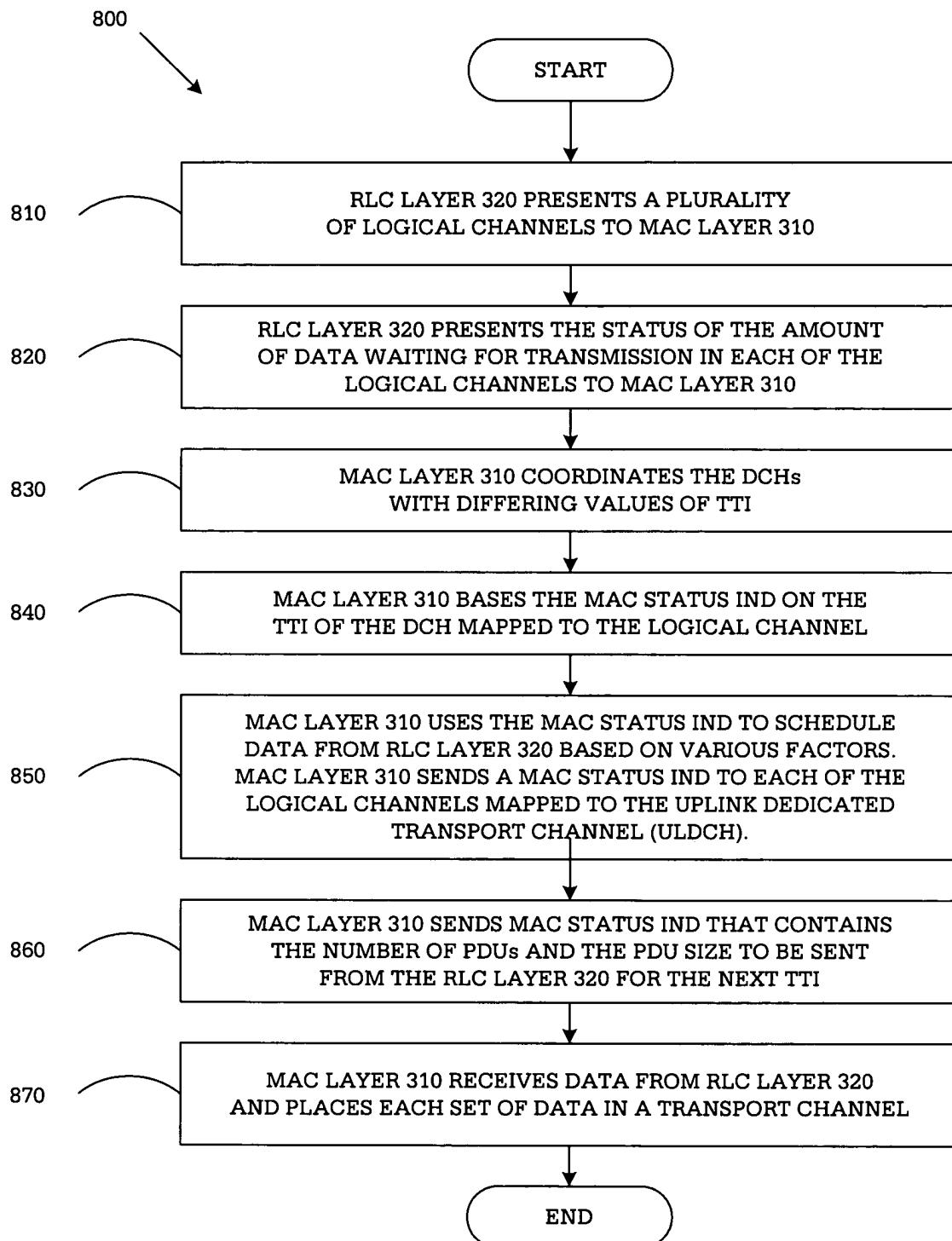
700



TFCI	TFI <sub>1</sub>	TFI <sub>2</sub>	TFI <sub>3</sub>	TFI <sub>4</sub>
0	0	0	0	0
1	1	0	0	0
2	0	1	0	0
3	1	1	0	0
4	0	0	1	0
5	1	0	1	0
6	0	0	0	1
7	0	1	1	0
8	0	1	1	1

FIGURE 7

PRIOR ART



**FIGURE 8**  
PRIOR ART

900



	DCH1	DCH2	DCH3	DCH4
TFCI	TFI LIST (TFC SET)			
0	1	0	0	0
1	0	1	0	0
2	1	1	0	0
3	0	1	1	0
4	1	1	1	0
5	0	2	0	0
6	1	2	0	0
7	0	2	1	0
8	1	2	1	0
9	0	2	1	1
10	1	2	1	1

FIGURE 9

03-SIN-091

10 / 13

1000



	DCH1	DCH2	DCH3	DCH4
TFCI	TFI LIST (TFC SET)			
0	1	0	0	0
2	1	1	0	0
4	1	1	1	0
6	1	2	0	0
8	1	2	1	0
10	1	2	1	1

FIGURE 10

1100



	DCH1	DCH2	DCH3	DCH4
TFCI	TFI LIST (TFC SET)			
6	1	2	0	0
8	1	2	1	0
10	1	2	1	1

**FIGURE 11**

1200



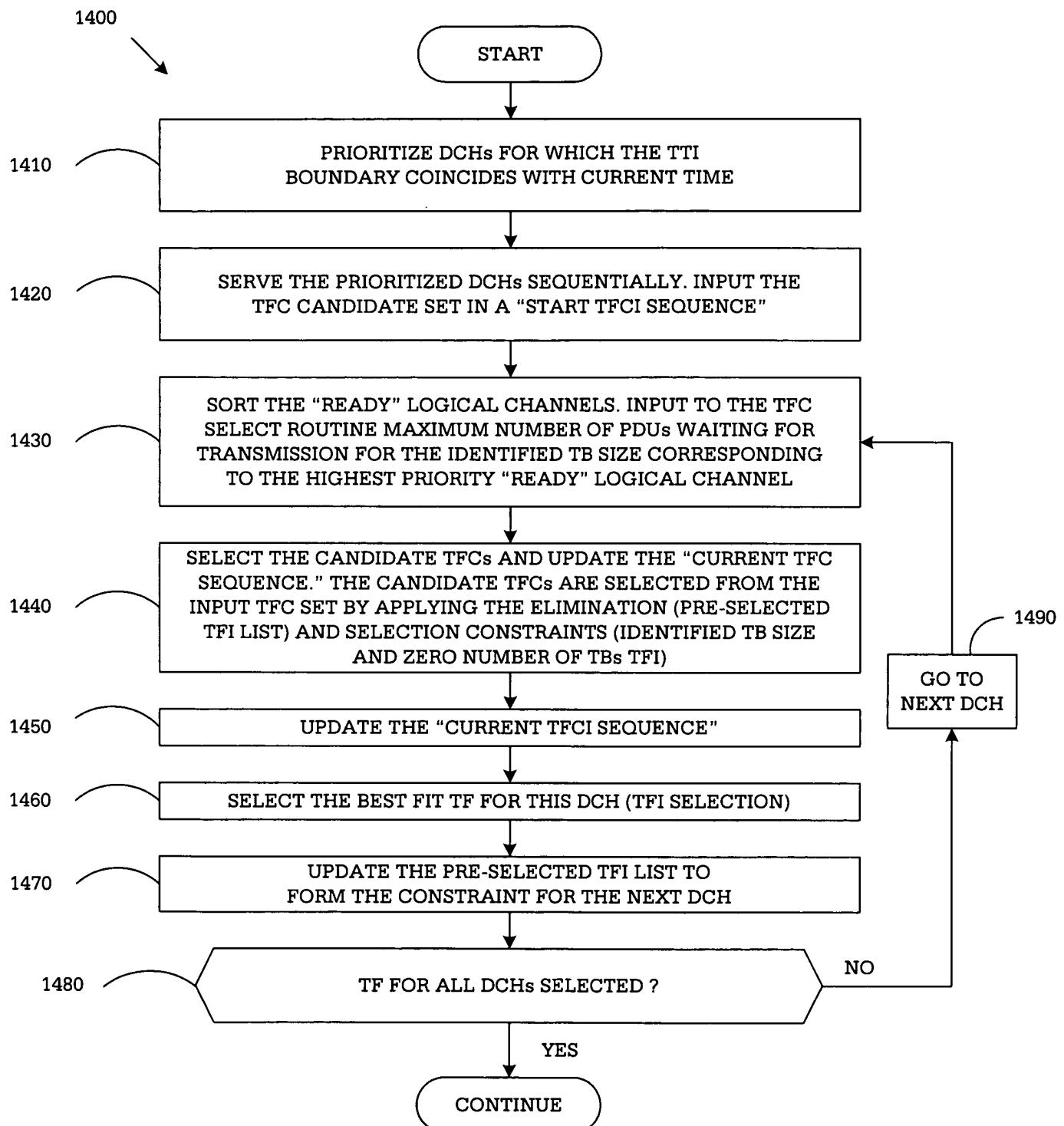
	DCH1	DCH2	DCH3	DCH4
TFCI	TFI LIST (TFC SET)			
8	1	2	1	0
10	1	2	1	1

1300

**FIGURE 12**


	DCH1	DCH2	DCH3	DCH4
TFCI	TFI LIST (TFC SET)			
8	1	2	1	0

**FIGURE 13**



## FIGURE 14

03-SIN-091

13 / 13

1500

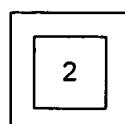
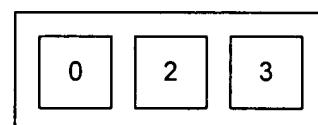
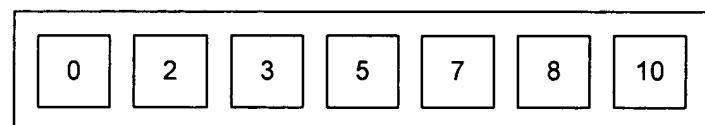
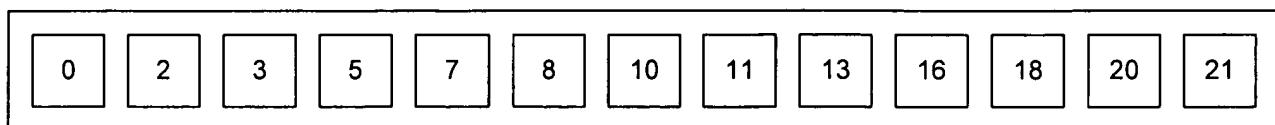


FIGURE 15